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vor Patents, Washington, DC 20231 Signed: Coa Q A - ac Do

Raquel Graeber

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

application of:

Yao, et al.

Serial No. 09/457,929

Filed: December 8, 1999

For: WAFER CARRIER AND

SEMICONDUCTOR APPARATUS

FOR PROCESSING A

SEMICONDUCTOR SUBSTRATE

Examiner: Jeffrie R. Lund

Group Art Unit: 1763

San Francisco, CA 94111

Date: August 21, 2001

70 1200 TO 120

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

The following is a preliminary amendment in the subject continuation application and is in response to a Final Office Action mailed May 21, 2001. Please amend the referenced application as follows:

IN THE SPECIFICATION

Please replace the paragraph beginning at page 7, line 3, with the following rewritten paragraph:

--To maintain the desirable line or point contact with the peripheral edge of the wafer and to provide secure support of the wafer, the thermal expansion of the wafer carrier is considered. Preferably, little thermal expansion occurs during the process so that the desired angle of the incline is preserved. Specifically, the wafer carrier is comprised of a material having an isotropic coefficient of thermal expansion in the range of 2.6×10^{-6} °C, with the lower values preferred. Materials with suitable coefficients of thermal expansion include silicon and silicon carbide.--.

the Table